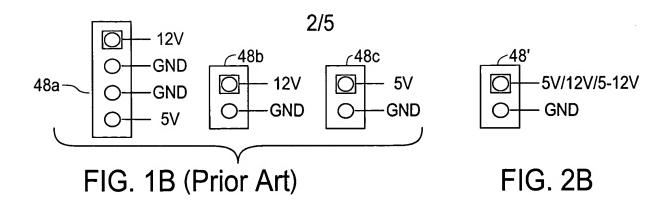
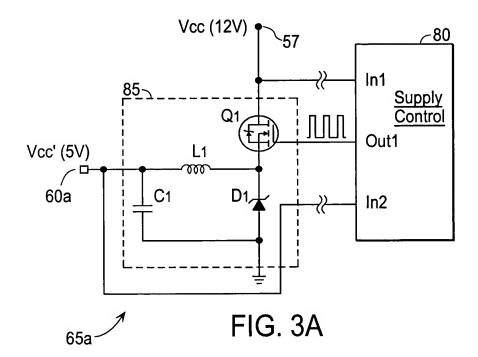
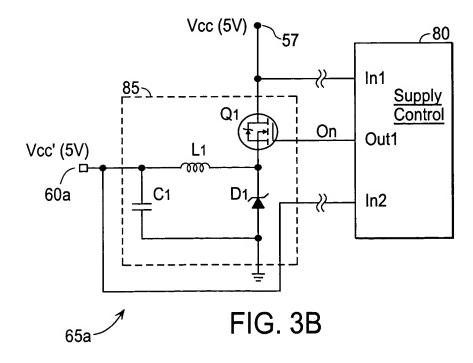


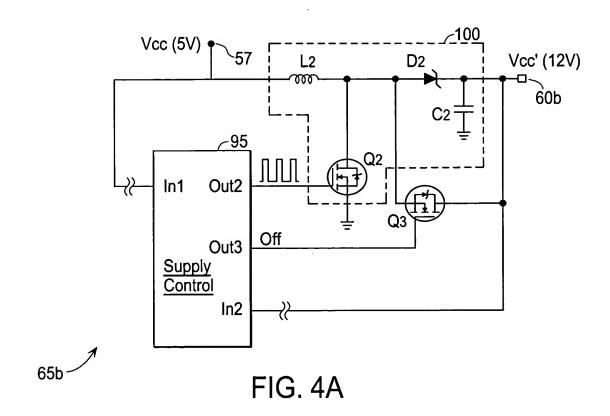
FIG. 2A

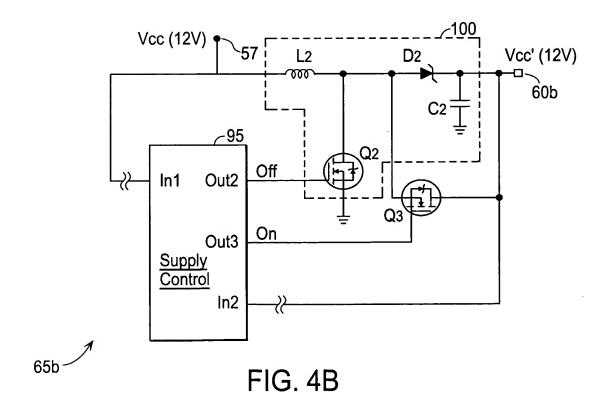




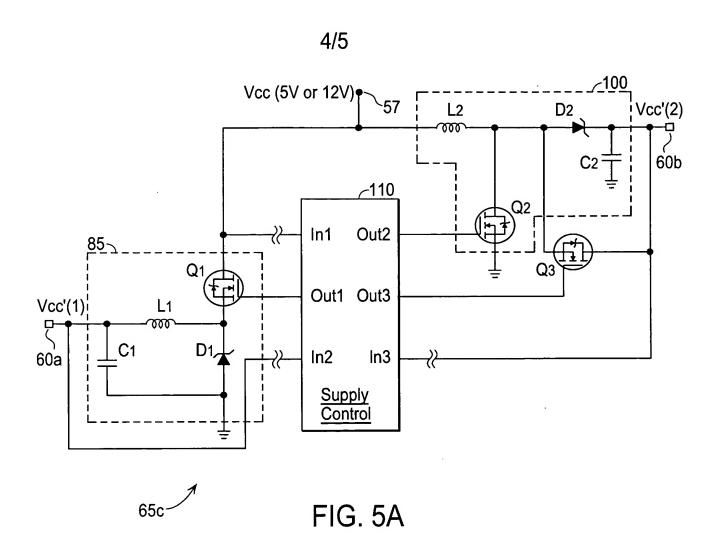


3/5





Sheet 4 of 5 Bryan S. Rowan Hitachi GST Docket No. HSJ920030138US1 (PWD)

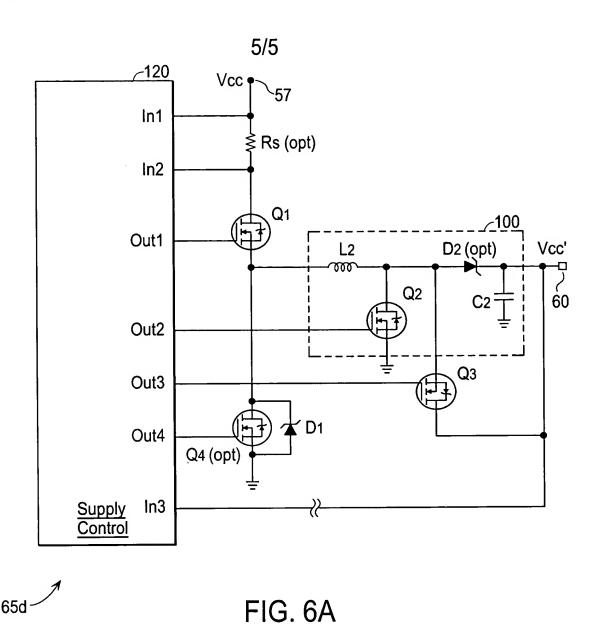


States of Output Signals Out1, Out2, and Out3 for Different Combinations of Vcc, Vcc'(1). and Vcc'(2)

Vcc	Vcc'(1)	Vcc'(2)	Out1	Out2	Out3
5V	5V	5V	On	Off	On
5V	5V	12V	On	Switch	Off
12V	12V	12V	On	Off	On
12V	5V	12V	Switch	Off	On

FIG. 5B

Sheet 5 of 5 Bryan S. Rowan Hitachi GST Docket No. HSJ920030138US1 (PWD)



States of Output Signals Out1, Out2, Out3, and Out3 for Different Combinations of Vcc and Vcc'

Vcc	Vcc'	Out1	Out2	Out3	Out4
< Vcc'	> Vcc	On	Switch	Off/Switch	Off
= Vcc'	= Vcc	On	Off	On	Off
> Vcc'	< Vcc	Switch	Off	On	Off/Switch

FIG. 6B